BI-WEEKLY STATUS REPORT

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| Subject: Visual Light Communication |  |
| Period Ending: 11/14/2018 |  |

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| ACTIVITIES COMPLETED THIS WEEK |
| **Completed Deliverables:**   * Built preliminary DC circuit with miniature LED in order to debug code logic * Researching different types of MOSFET to use in order to control the 12 volt LED * Debugged much of the VLC software and made some enhancements * Added preamble (send of all high bits) * Fixed several transmitter issues involving misreading of the FIFO buffer (changed so that data is not continually added at the beginning but rather added once at the beginning and is continually read * Message correctly sends for a short period of time but overall there is still a lot of garbage data that ends up getting through |

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| ACTIVITIES TO BE STARTED NEXT WEEK |
| * Software issues * There seem to be some issues with a while loop in the transmitter not fully running during the busy wait. * Synchronizing issues with the preamble and the receiver still seem to be present (last bit of preamble seems to be decoded as first bit of the message) * Issues in calibrating the threshold value of the preamble * Built DC circuit with DC LED * Fritz circuit diagrams for all the hardware setups we built |

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| LONG TERM PROJECTS |
| * Debug VLC software |